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Title: **METHOD AND SYSTEM FOR SIMULATION OF DIGITAL/ANALOG INTERFACES WITH ANALOG TRI-STATE IOPUTS**

Inventor(s): Alexander D. Schapira et al.

DOCUMENTS BEING FILED:

The PTO stamp hereon acknowledges receipt of:

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I hereby certify that this paper (along with any referred to as being attached or enclosed) is being hand delivered on the date shown below to the Commissioner for Patents, Washington, D.C. 20231.

February 26, 2001
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